

10/802809















Inventor: SAITO , HITOSHI

Status: 71 - RESPONSE TO NON-FINAL OFFICE ACTION ENTERED AND FORWARDED TO EXAMINER
Title: SEMICONDUCTOR DEVICE

Examiner: DINH, SON

GAU: 2824
Classification: 365/145.000

Incoming tab report (15 items, not sorted)

Img	Status	Doc Code	Document Type	Receipt Date	Pages	Annotations
	7	A...	Amendment - After Non-Final Rejection	06/22/2005	2	
	7	CLM	Claims	06/22/2005	5	
	7	REM	Applicant Arguments or Remarks Made in an Amendment	06/22/2005	2	
	7	XT/	Extension of Time	06/22/2005	1	
	7	TRNA	Transmittal letter	03/18/2004	3	
	7	SPEC	Specification	03/18/2004	33	
	7	CLM	Claims	03/18/2004	3	
	7	ABST	Abstract	03/18/2004	1	
	7	DRW	Drawings	03/18/2004	13	
	7	OATH	Oath or Declaration filed	03/18/2004	3	
	7	A.PE	Preliminary Amendment	03/18/2004	1	
	7	SPEC	Specification	03/18/2004	1	
	7	REM	Applicant Arguments or Remarks Made in an Amendment	03/18/2004	1	
	7	IDS	Information Disclosure Statement (IDS) Filed	03/18/2004	2	considered 2/20/05

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Img	Status	Doc Code	Document Type	Receipt Date	Pages	Annotations
	7	FRPR	Foreign Priority Papers Filed	03/18/2004	45	

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((ferroelectric adj capacitors) and (switching adj transistor) and (MOS adj transistor) and (plate adj line) and (plate adj driver adj circuit)).clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 12:11
L2	1173	saito-hitoshi.inv.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 12:12